

### The Evolution of the IBM i Architecture



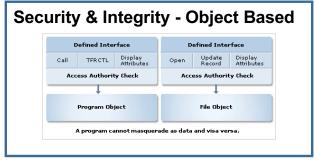
Steve Will
IBM i Chief Architect
@Steve\_Will\_IBMi

http://bit.ly/you\_and\_i\_blog

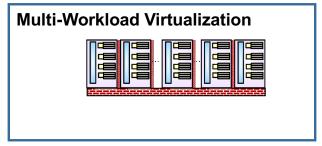
#### IBM i Architecture



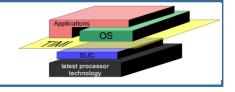








**Technology Independent Machine Interface** 



A system designed for business

### Hey! Wait a minute.

## Hey! Wait a minute.

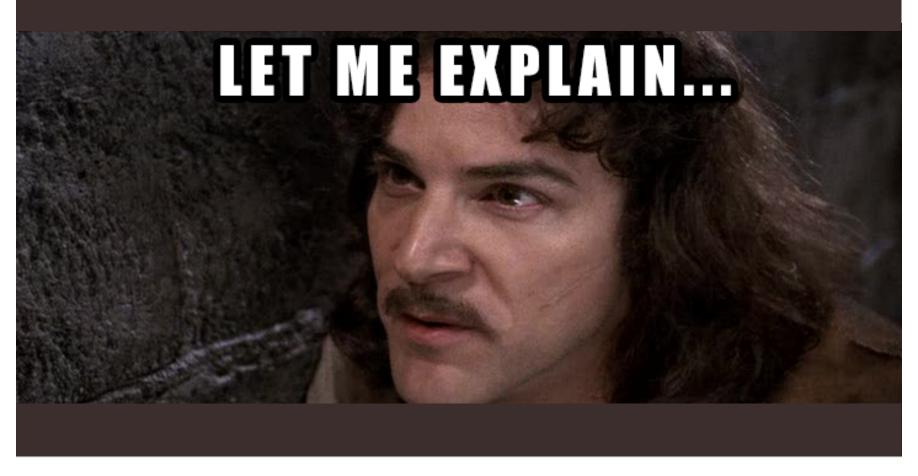


## Hey! Wait a minute.











2000

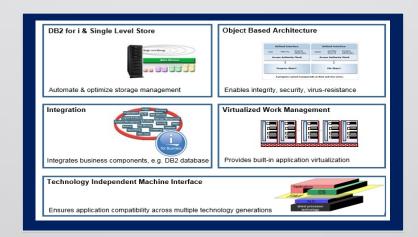




1988 AS/400®

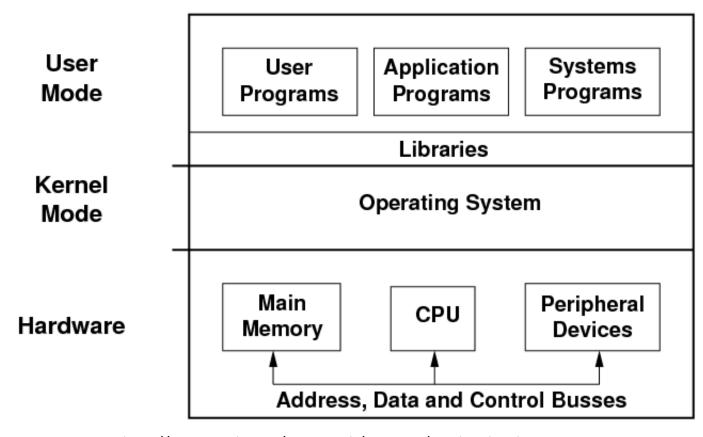
System/38 (1978) System/36 (1983)

# ARCHITECTURE



#### General View of an OS Architecture

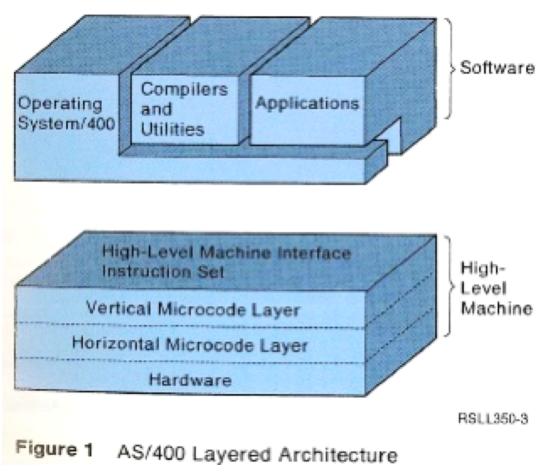




http://minnie.tuhs.org/CompArch/Lectures/week01.html

#### Layered Architecture of OS/400





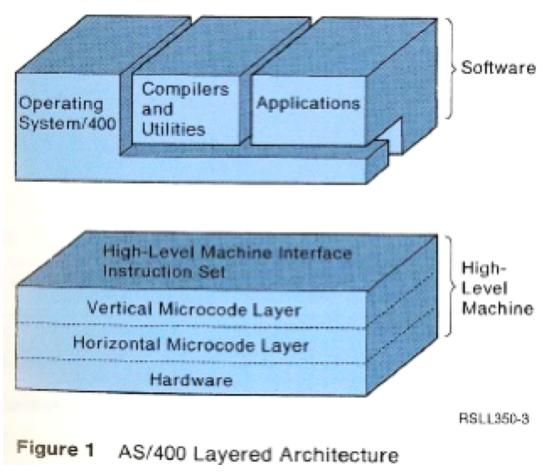
Applications are compiled to an intermediate language, not processor instructions.

The "MI" (or "TIMI") is the defined set of these instructions.



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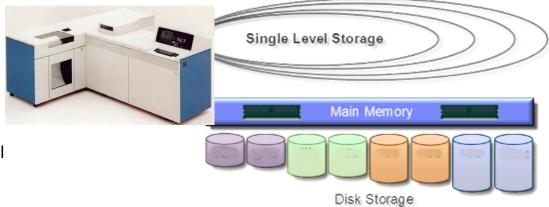


#### Storage Model – Back to System/38



All storage on the system is treated as a single contiguous set of memory, so mapping storage required special methods and knowledge of storage devices.

System/38 and initial AS/400 used 48-bit addresses for what became known as "Single Level Storage."



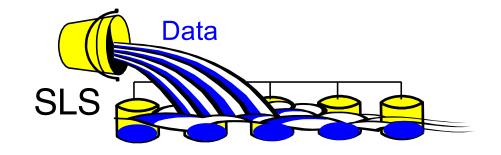


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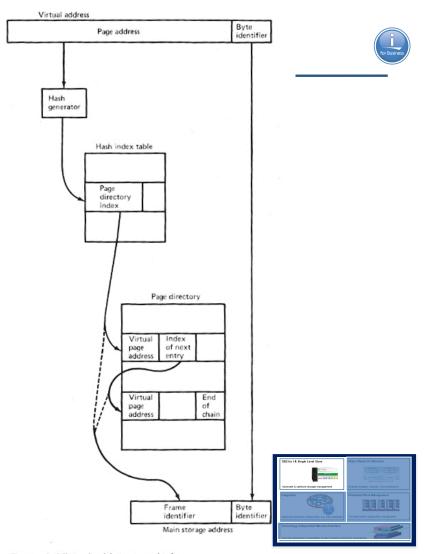


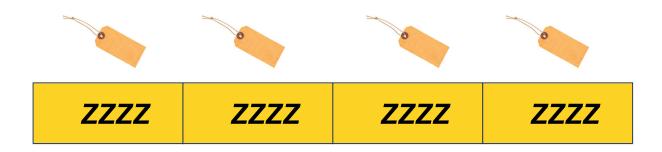
Figure 1 Virtual address translation



ZZZZ ZZZZ	ZZZZ	ZZZZ
-----------	------	------





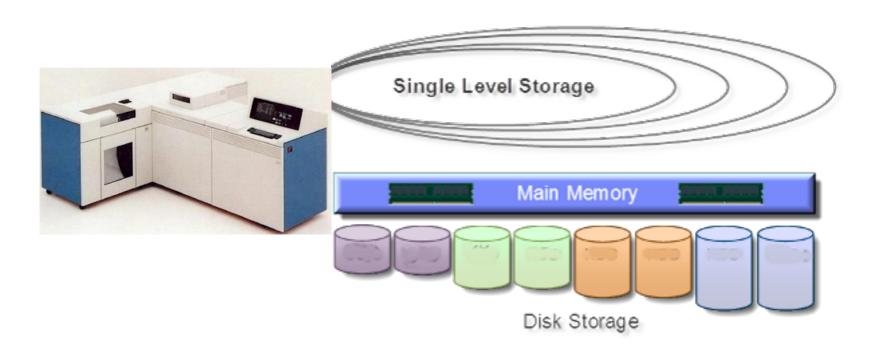


A "tag" per 32-bit word to indicate it's part of a pointer.



#### System/38 Single Level Storage

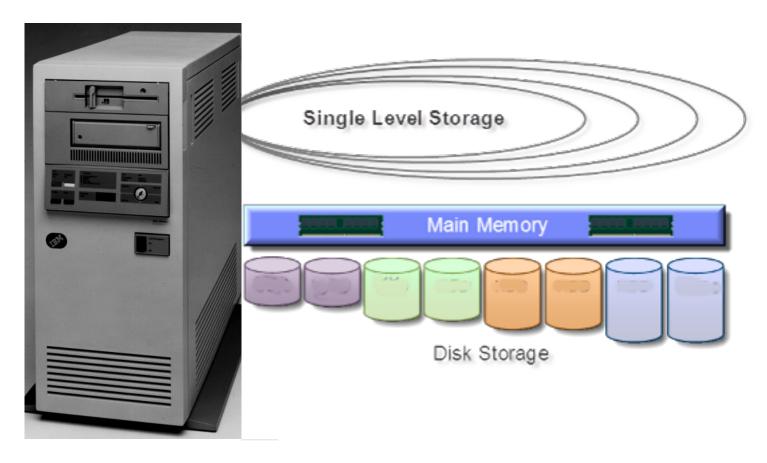






#### AS/400 Single Level Storage – The Same?







#### From System/38 to IBM i – "Single" Changed

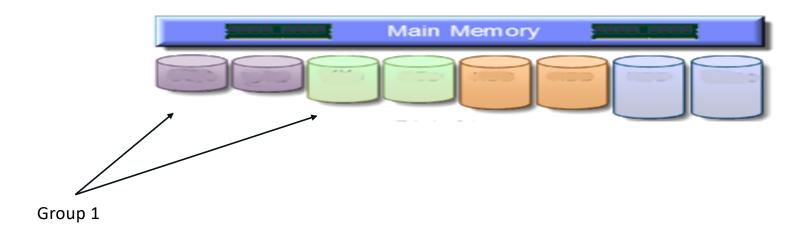






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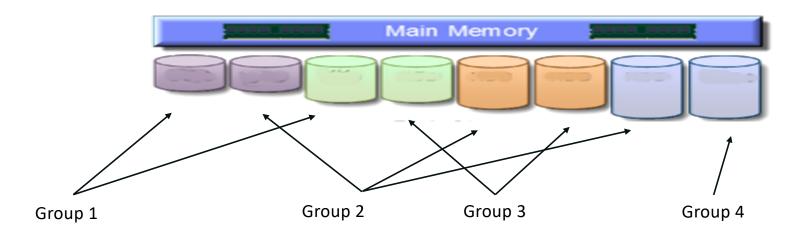


The AS/400 architects decided they wanted to have groups of disks.



#### From System/38 to IBM i – "Single" Changed





The AS/400 architects decided they wanted to have groups of disks.

These groups were called Auxiliary Storage Pools.



#### Integrated DB



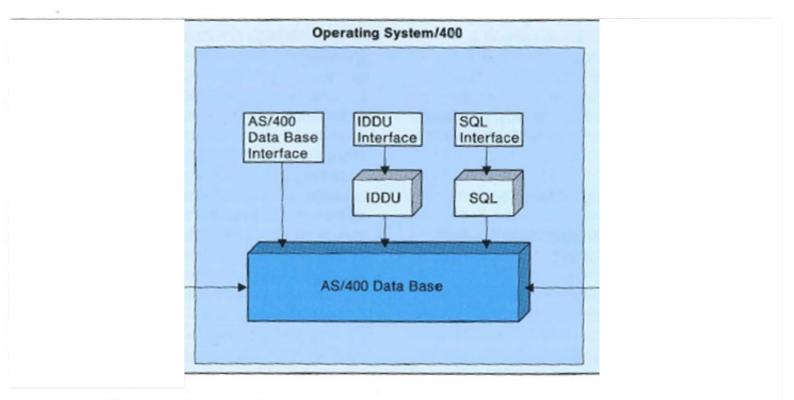


Figure 1 Interface to AS/400 Data Base



#### Integrated DB



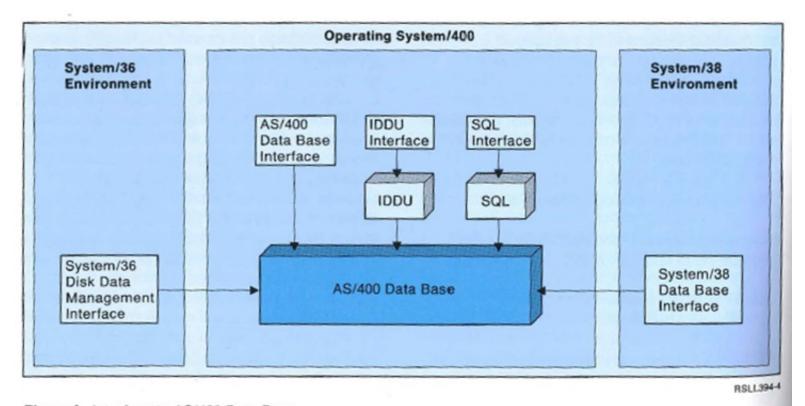


Figure 1 Interface to AS/400 Data Base



#### **Object Orientation**



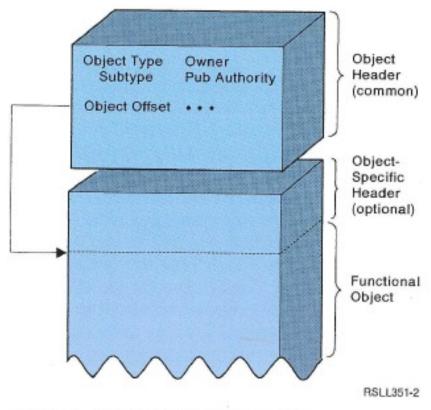


Figure 2 Structure of Generic Object

Objects protect the integrity of the system and customer data, while also allowing a strict object-based security architecture.



#### What About Integration & Work Management?



#### What About Integration & Work Management?





#### So, AS/400 V1R1 had the perfect architecture ...

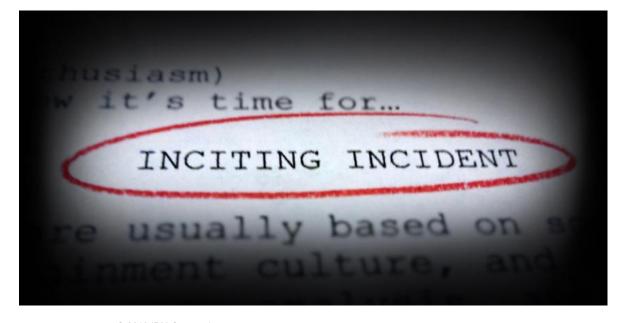




### And then ...



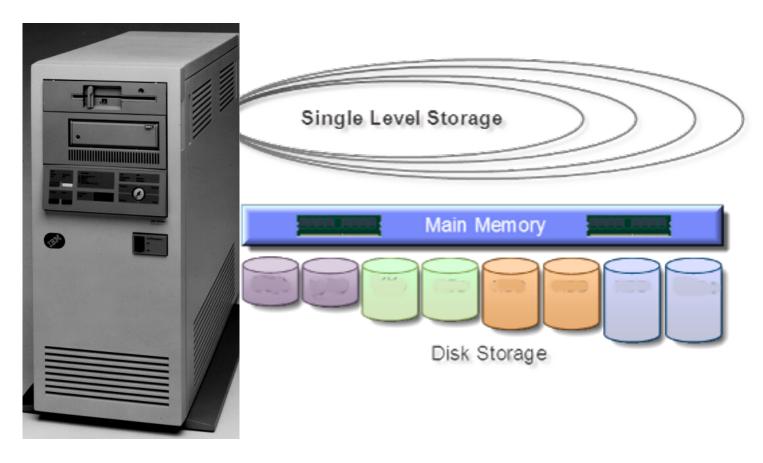
### And then ...



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#### Remember that Single Level Storage Thing?









First: RPG GOBOL



RPG GOBOL
THE

PROGRAMMING LANGUAGE

But Then:



RPG GOBOL

But Then:

First:





(Pointers!)



First: RPG GOBOL

But Then:

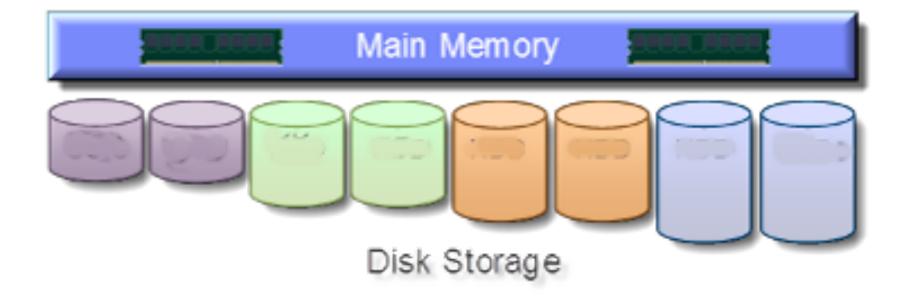




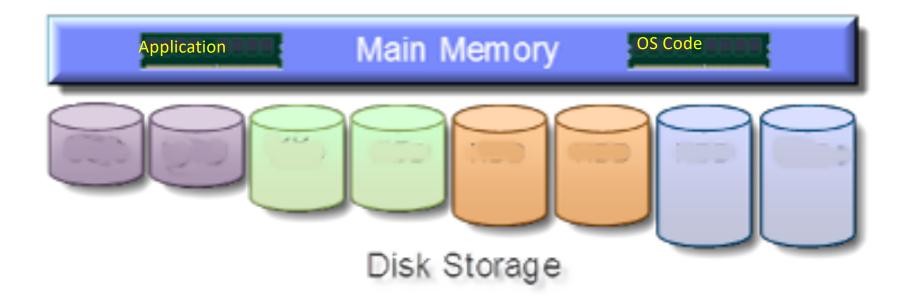
(Pointers!)

So What?



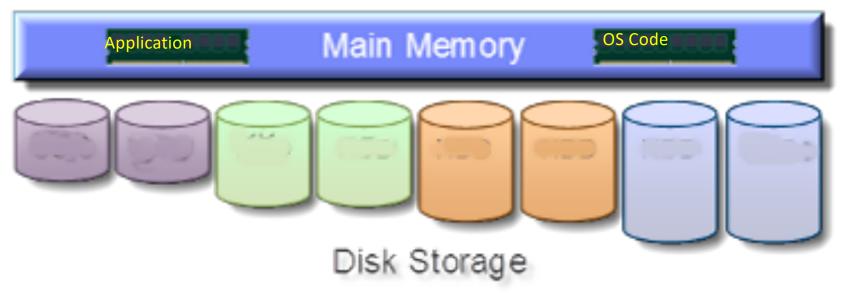




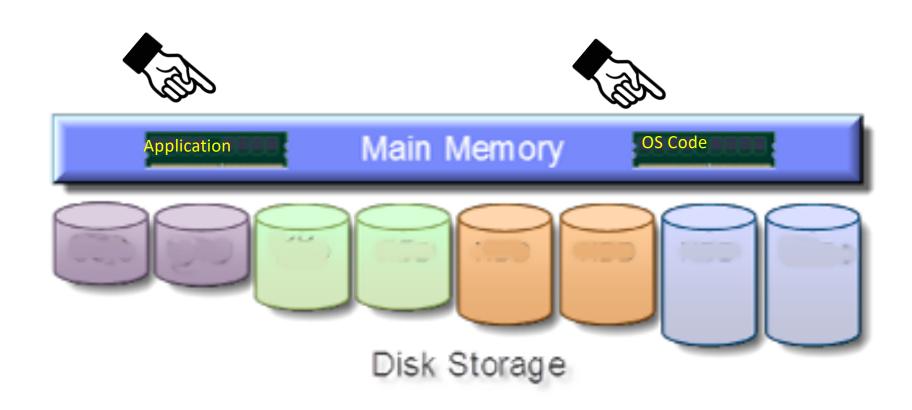












### Programming Languages and Integrity



First: RPG GIBI

But Then:



So ...

Security level 50 & initial HW support for Integrity (Storage Protection, Privileged/Problem state support)



# File Systems



- System/38 had one file system QSYS.LIB which was (is) "flat"
  - By the late '70s, hierarchical file systems were proving their value



### File Systems



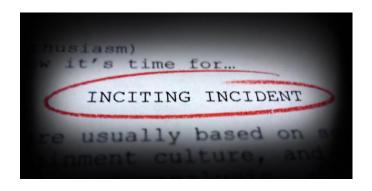
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- System/36 implemented QDLS and AS/400 included it as a system-wide architecture
  - So, again, the AS/400 architecture was more than just the S/38



#### File Systems



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  - By the late '70s, hierarchical file systems were proving their value
- System/36 implemented QDLS and AS/400 included it as a system-wide architecture
  - So, again, the AS/400 architecture was more than just the S/38
- The industry defined a standard hierarchical file system, as part of the POSIX standards

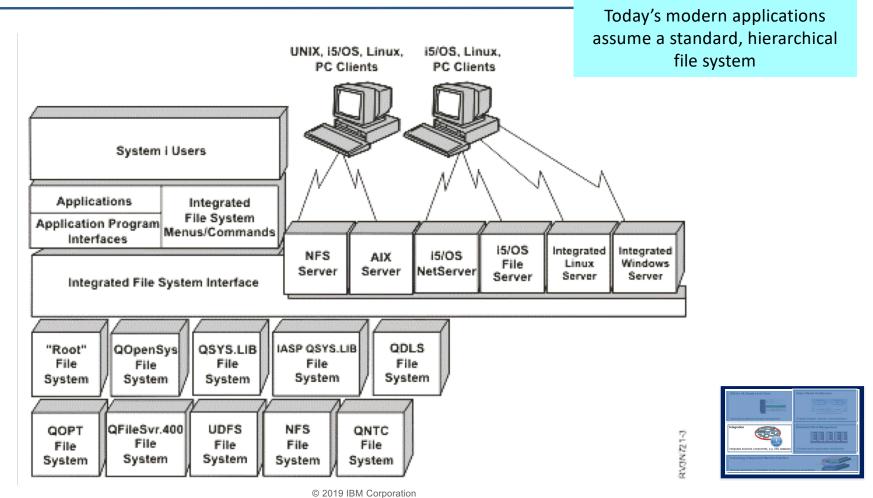


**POSIX** 



# Integrated File System - IFS

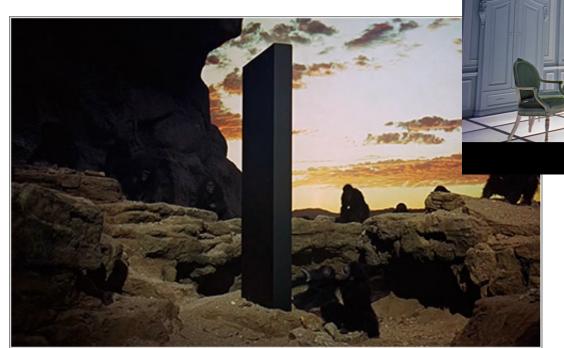




Once Upon a Time

for Business

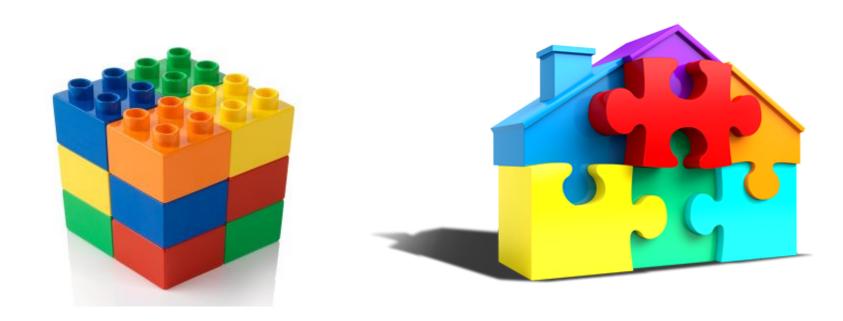
For very good reasons, early software was written in large, **monolithic**, programs.



The Monolith

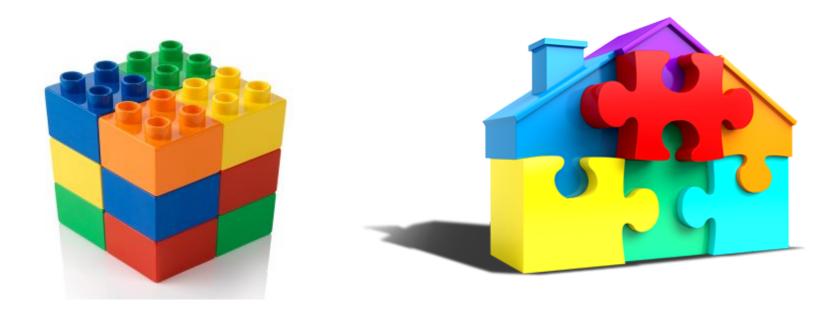
# But the Programming World Kept Changing





# But the Programming World Kept Changing

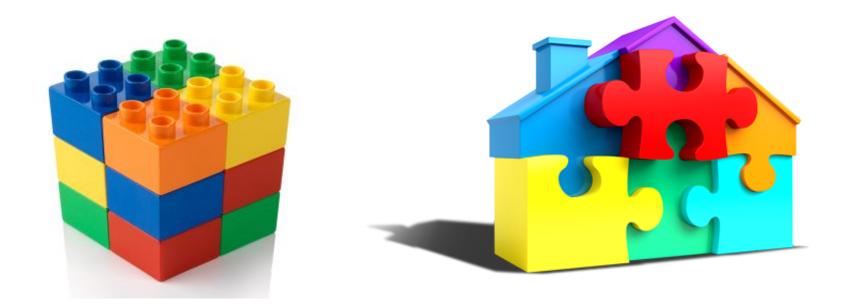




Computer architectures made resources more available, compilers made calls between programs more efficient, and languages developed to assume **modularity**.

# But the Programming World Kept Changing





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#### The Integrated Language Environment: aka ILE, aka New Program Model



The creation of ILE required the creation of the Activation Engine

- A new architectural component
- Controls the birth, life, and death of the process and its activation groups

#### **ILE Benefits**

- Binding
- Modularity
- Reusable Components
- Common Runtime Services through bindable APIs
- Source Debugger
- Better Control Over Resources

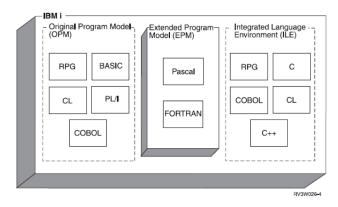
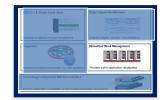


Figure 2-5. Relationship of OPM, EPM, and ILE to IBM





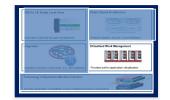
- Job: the user-visible, user definable container of work
- Process: the underlying above-MI construct for processing, which has information about
  - The process itself
  - What's running in the process





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- Both of these take extensive time to set up







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Applications wanted to kick off work asynchronously and frequently.







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# **Lotus** ••• Notes



















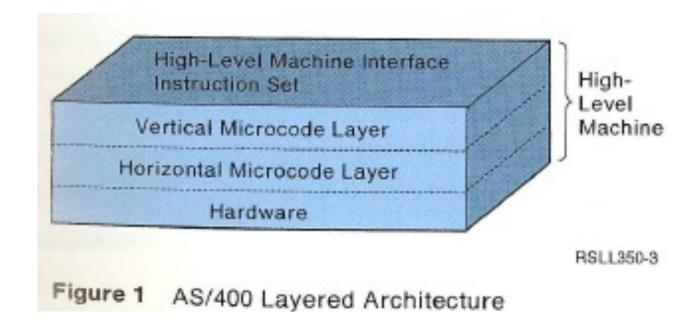












The original AS/400 hardware was a 48-bit processor, which implemented a "Complex Instruction Set."





# **Technology Independent Machine Interface (TIMI)**

Vertical Machine Code (VMC)

Horizontal Machine Code (HMC)

48-bit CISC Processor





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### **Technology Independent Machine Interface (TIMI)**

64-bit RISC Processor





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Horizontal Machine Code (HMC)

48-bit CISC Processor

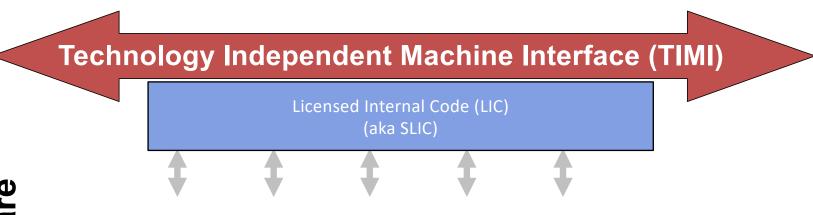
### **Technology Independent Machine Interface (TIMI)**

Licensed Internal Code (LIC)
(aka SLIC)

64-bit RISC Processor







Hardware

So, did the "Architecture" change?





# **Technology Independent Machine Interface (TIMI)**

Licensed Internal Code (LIC)
(aka SLIC)

\$







Hardware

So, did the "Architecture" change?

Above TIMI?

Not so much.





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So, did the "Architecture" change?

Above TIMI? Not so much. Below TIMI? You betcha!





### **Technology Independent Machine Interface (TIMI)**

Licensed Internal Code (LIC)
(aka SLIC)

1











Processor technology: 48-bit CISC to 64-bit RISC SMP: from 0.2 core up to 192 cores; 768 threads Memory and Disk: Increases in capacity and density I/O adapters: SPD to PCI to PCI-X to PCIe

Future Technology

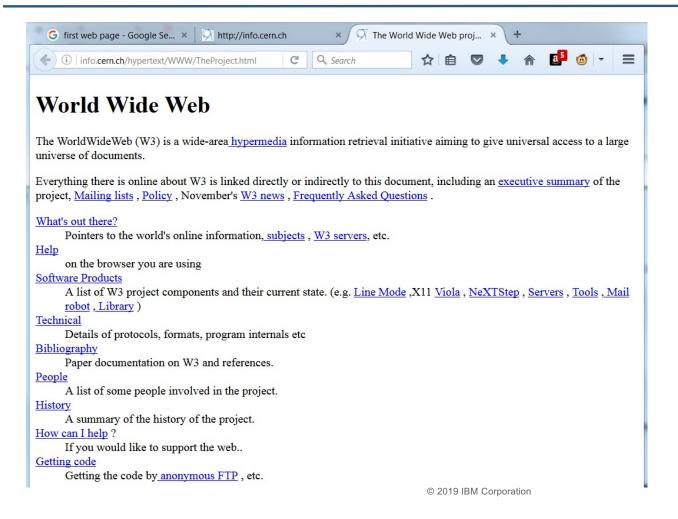
#### So, did the "Architecture" change?

Above TIMI? Not so much. Below TIMI? You betcha!



#### And Then Came This ...





Hypertext Transfer Protocol (HTTP)

The first web page ever!

# Web Serving Drove Changes



- HTTP was gaining strength as a presentation protocol
  - With imbedded data retrieval from a web server
- V3R7 CERN-based web server
  - Single instance only; V4R1 Multiple servers allowed
- V4R5 Apache-based web server



# Web Serving Drove Changes



- HTTP was gaining strength as a presentation protocol
  - With imbedded data retrieval from a web server
- V3R7 CERN-based web server
  - Single instance only; V4R1 Multiple servers allowed
- V4R5 Apache-based web server
- Benefited from Multi-threading
- Drove requirements for
  - Digital Certificates
  - Server Architecture
  - Teraspace



#### What about the Database?





# The Classic Query Engine - CQE





DB2 Answer





DB2 Architects wanted a Query Engine which could



- DB2 Architects wanted a Query Engine which could
  - Take advantage of knowing it was dealing with SQL





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Learn from the past

SQL





- DB2 Architects wanted a Query Engine which could
  - Take advantage of knowing it was dealing with SQL

Learn from the past

 Use what it learned from queries for one application to improve queries for other applications. SQL

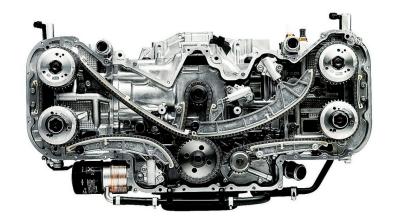




# The SQL Query Engine



User Question in the Form of an **SQL** Query



DB2 Answer



# **Avoiding Disruption Was Paramount**







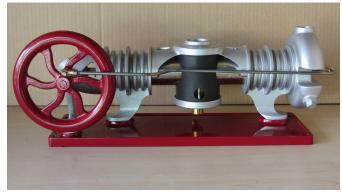
Time



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Time



# **Avoiding Disruption Was Paramount**

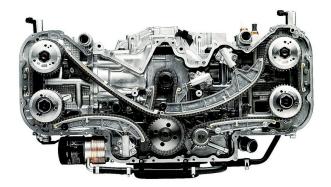










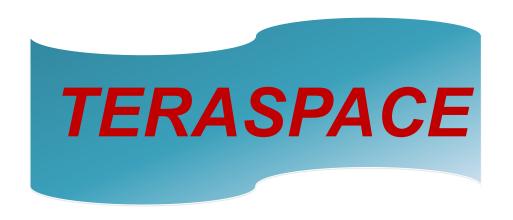




#### Time







#### Teraspace: Storage Revolution

- 1 Terabyte (= 2<sup>40</sup> bytes)
  - flat (non-segmented), process-local storage
  - Temporary
- Lightweight 8-byte pointers available
  - high performance, untagged
- And definitely NOT Single-Level Store!





2000





1988 AS/400®

System/38 (1978) System/36 (1983)





6.1



#### Retranslation at 6.1



In IBM i 6.1, Retranslation occurred to for three purposes:

- Improving security & integrity
- Performance
- Removing limits



# **Technology Independent Machine Interface (TIMI)**

Licensed Internal Code (LIC)
(aka SLIC)

64-bit RISC Processor



# 6.1 Retranslation Integrity Part 1: The Execution Stack





User Programs

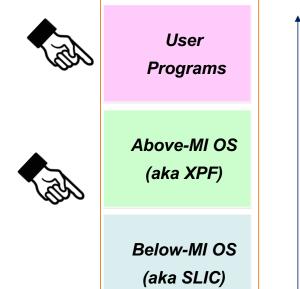
Above-MI OS (aka XPF)

Below-MI OS (aka SLIC)

Before 6.1 Single Stack

# 6.1 Retranslation Integrity Part 1: The Execution Stack

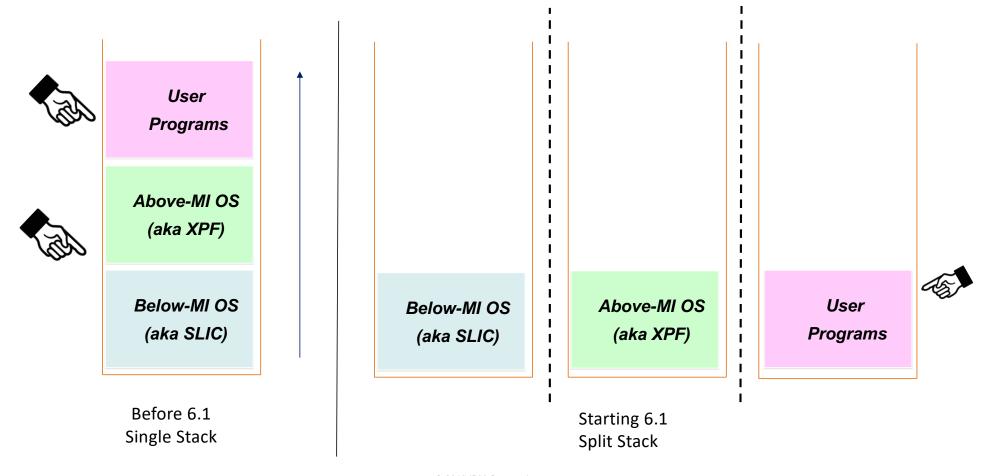




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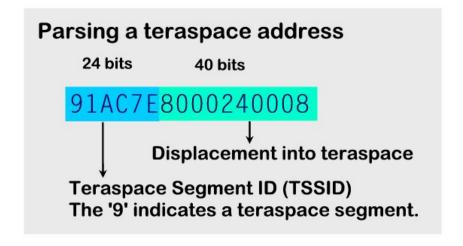




#### Retranslation Integrity & Performance Part 2: Teraspace



- Teraspace initially implemented in software – in SLIC
- Over time, processor support was added to improve use and integrity of addressing.
- By 6.1, all supported machine types had the necessary processor support to do teraspace "right."
- Retranslation accomplished true "hardware protection" and use of teraspace.





## Retranslation Integrity Part 3



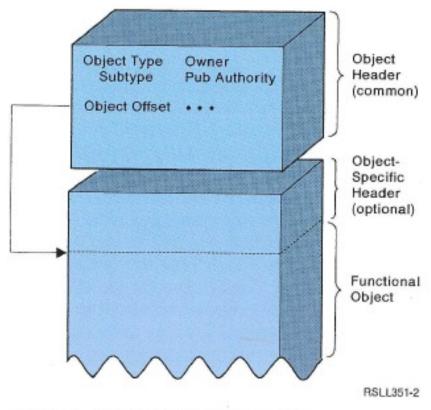


Figure 2 Structure of Generic Object

To protect the integrity of objects:

- Privileged/Problem domain
- User/System state
- 6.1: Hardware Protection for accessing privileged domain from user state
- 6.1: Close any potential way to masquerade as system state.



# PASE – Portable Application Solutions Environment



• By 2000, AIX and OS/400 were able to run on the same POWER processors.



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- This created the possibility for executables which are MI-based and AIX-based to run on the same hardware in the same partition.
- PASE makes it possible for those binaries to run in the same process.



#### PASE – Portable Application Solutions Environment



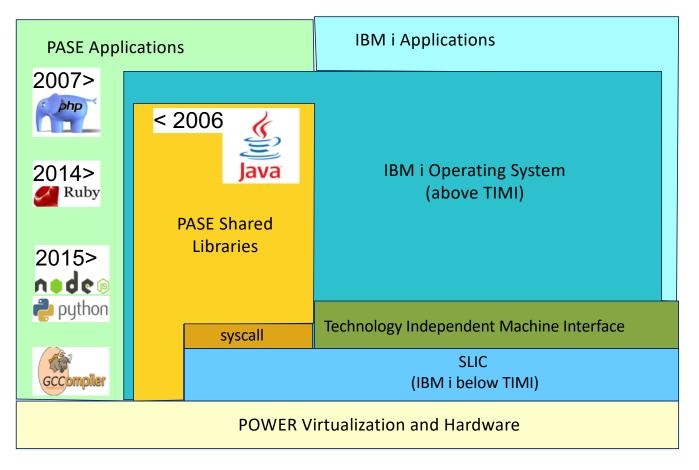
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- PASE makes it possible for those binaries to run in the same process.

- PASE is a release of AIX
  - Fitted to talk to SLIC rather than directly to the AIX kernel.
- PASE gets the memory from same SLIC teraspace pools used by ILE
  - for program run stack, heap, and shared memory
  - PASE can ONLY see memory that
     PASE acquired through its own syscall
     APIs



#### Pictorial View of PASE





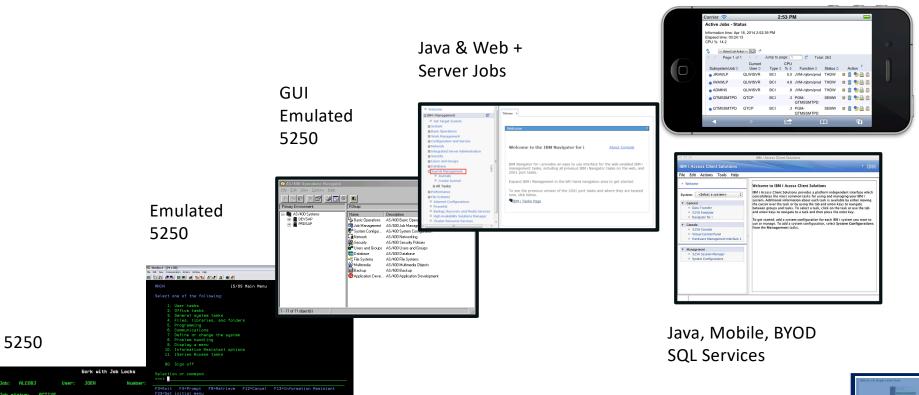
#### PASE allows IBM i to host

- Java
  - SAP
- PHP
- Ruby
- And many other open source options



# Managing/Accessing the System

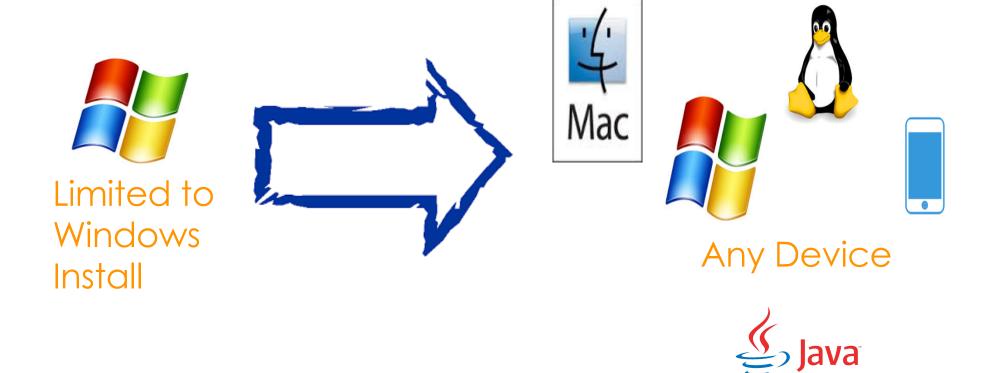






# IBM i Access and Management





## Some things I'll just mention



**Development Tools** 

**Object Signing** 

• Logical Partitioning

National Language Support • Enterprise Identity Mapping

• N-2 Support

Display & Print

Scaling

Technology Refreshes

LDAP

**Nodal Affinity** 

Evolution to Waiting Servers

Independent ASPs

• Integration of Open Source

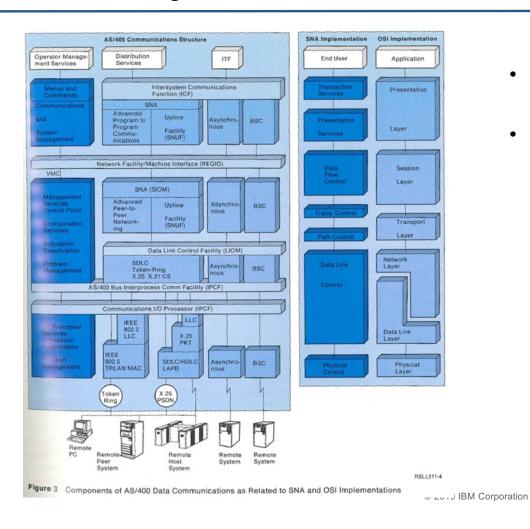
## And things I'll mention for a particular point



- Storage Technology
  - From 520-byte proprietary, to 512-byte commodity, to SAN
- I/O
  - During the CISC-to-RISC transition, the move from IOP-based I/O to IOA-based I/O was happening
- Networking
  - O. M. G. Does anyone remember **SNA**? Used to be interwoven. I mean, look at this:

## AS/400 Integrated Data Communications



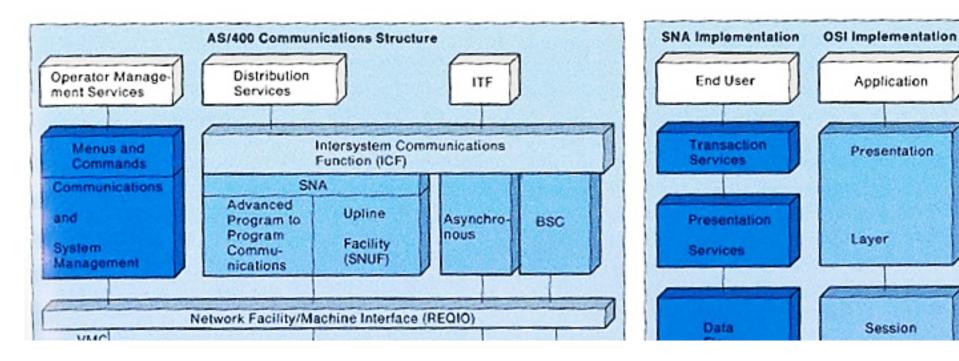


- · You can't read that, can you?
- Let's take a closer look at part of it.



## AS/400 Integrated Data Communications







Application

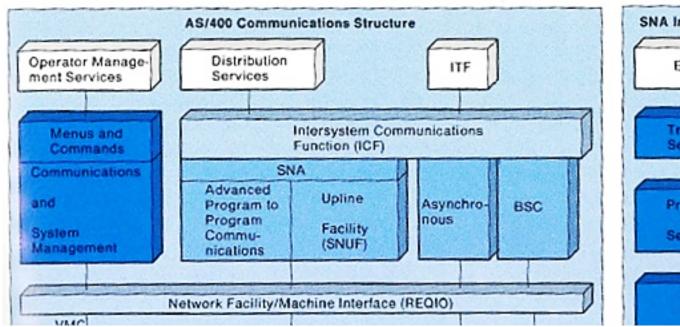
Presentation

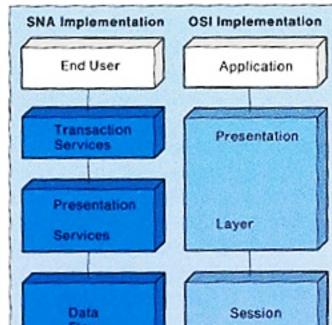
Session

Layer

## AS/400 Integrated Data Communications







No TCP/IP in sight.
The REQIO TIMI instruction survived.



## And things I'll mention for a particular point



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Major pieces of the architecture of S/38 & AS/400: Gone or transformed to be nearly unrecognizable.



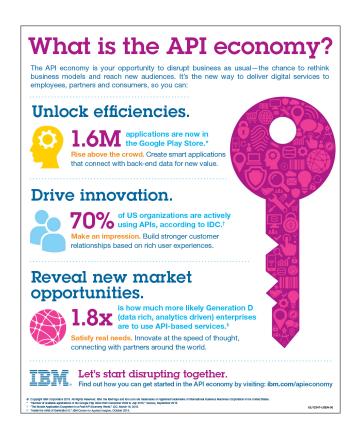
# Willat 1 MOWE







# **Continuous Availability**



# IBM Db2 Mirror for i



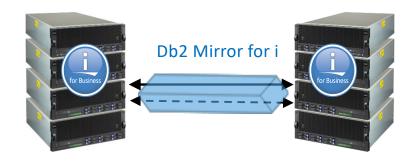
Operating System Synchronous Replication

Continuous Availability

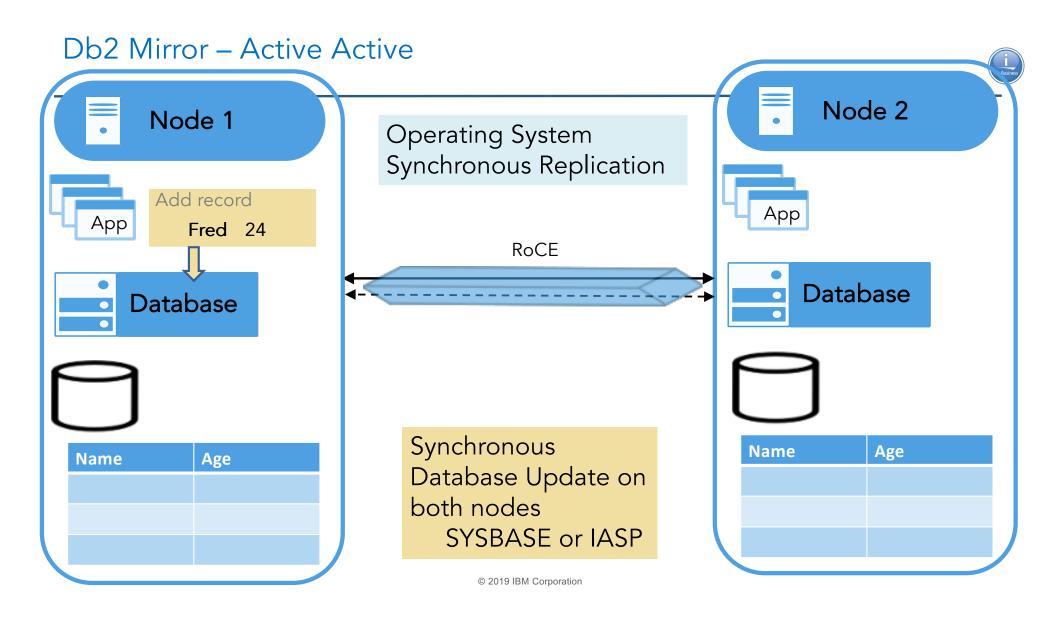
24 x 7 Up Time

Rolling Upgrades

RTO/RPO Near Zero



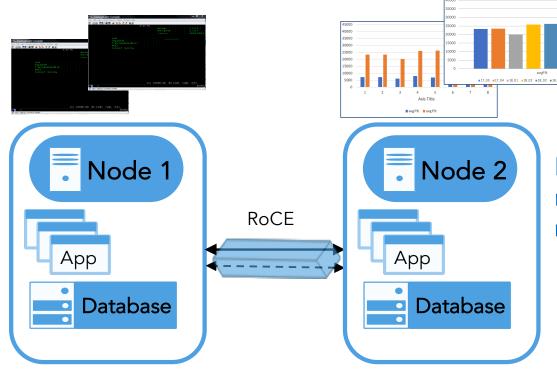
POWER8 or later & IBM i 7.4



# Db2 Mirror – Active Passive



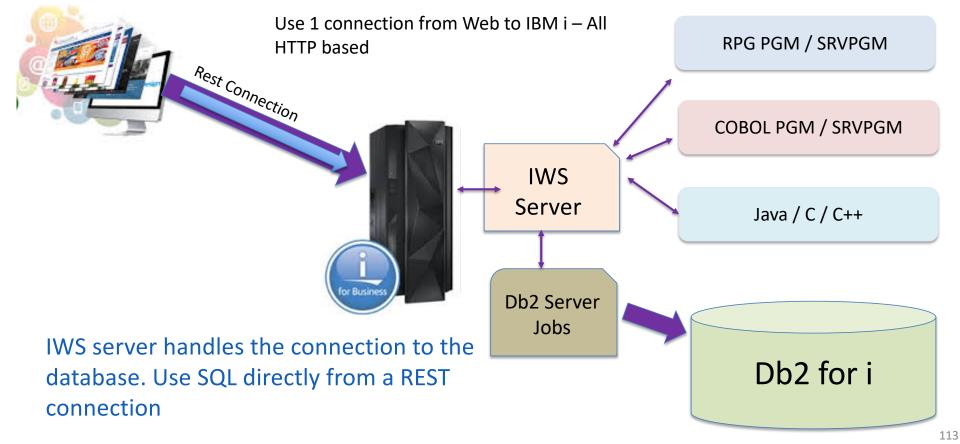
Run Production Workloads on this node



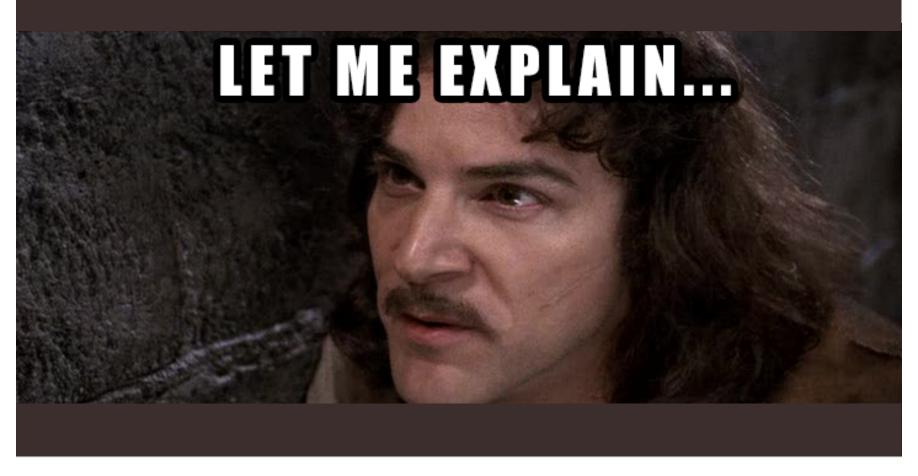
Run Queries and reports on this node

#### IBM i in the "Services" Era

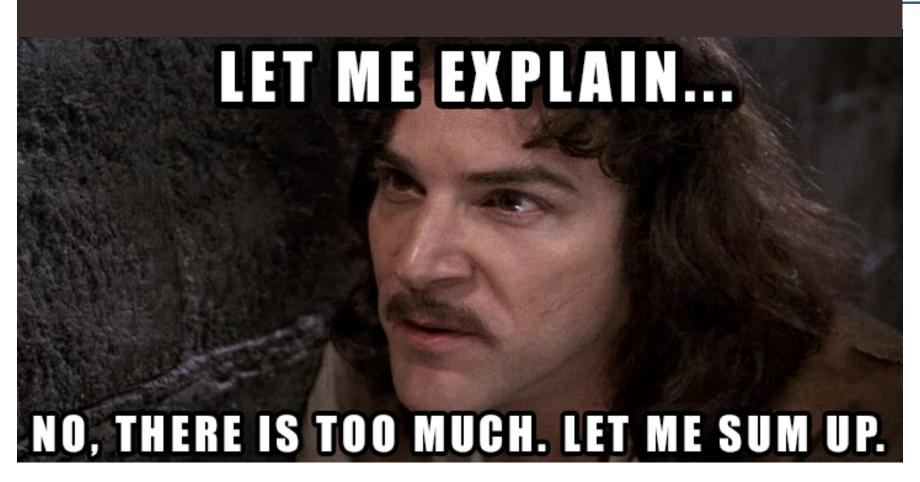










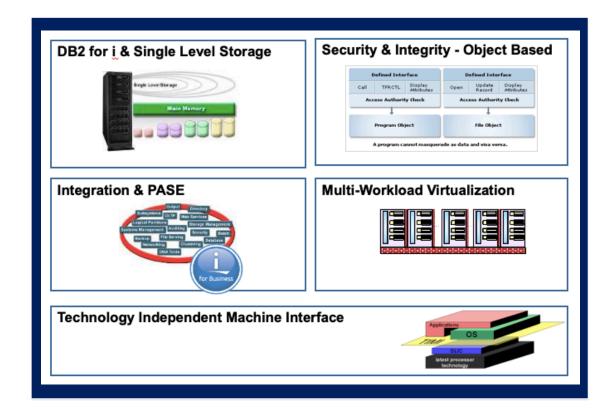






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IBM i - "Technology will change and IBM i is built to change with it"

<u>Link</u>



